PATENT ABSTRACTS OF JAPAN

(11)Publication number:

11-088080

(43) Date of publication of application: 30.03.1999

(51)Int.CI.

H03F 3/60

(21)Application number: 09-248327

(22)Date of filing:

12.09.1997

(71)Applicant: MITSUBISHI ELECTRIC CORP

(72)Inventor: CHAGI SHIN

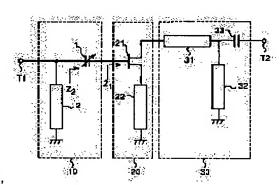
NAKAJIMA YASUHARU

(54) HIGH FREQUENCY AMPLIFIER CIRCUIT AND MICRO WAVE INTEGRATED CIRCUIT

(57)Abstract:

PROBLEM TO BE SOLVED: To improve design efficiency without considering a low frequency at the time of design by using a short stub whose other end is directly grounded.

SOLUTION: In a high frequency amplifier circuit, an input matching circuit 10 is provided with a variable capacitance element 1 whose one end is connected to the gate terminal of a transistor 21 and the short stub 2 whose one end is connected to the other end of the variable capacitance element 1 and whose other end is directly grounded. Since the variable capacitance element 1 is used in series for matching an input impedance in the input matching circuit 10, the transistor 21 and the short stub 2 are opened for direct current. Thus, the other end of the short stub 2 can directly be grounded without using a capacitor. Thus, the other end of the short stub 2 is directly connected to ground and therefore shorting effect can be obtained in a DC component. Thus, the gain in the low frequency band of not more than an MHz band can effectively be suppressed and the stable operation of the circuit is realized.



LEGAL STATUS

[Date of request for examination]

04.03.2003

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application

converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of

rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office